	Application No.	Applicant(s)
ALAS PAHALAM	10/650,653	RAJASHEKARA ET AL.
Notice of Allowability	Examiner	Art Unit
:	Michael Rutland-Wallis	2835
The MAILING DATE of this communication appears on the cover sheet with the correspondence address All claims being allowable, PROSECUTION ON THE MERITS IS (OR REMAINS) CLOSED in this application. If not included herewith (or previously mailed), a Notice of Allowance (PTOL-85) or other appropriate communication will be mailed in due course. THIS NOTICE OF ALLOWABILITY IS NOT A GRANT OF PATENT RIGHTS. This application is subject to withdrawal from issue at the initiative of the Office or upon petition by the applicant. See 37 CFR 1.313 and MPEP 1308.		
1. This communication is responsive to <u>08/28/2003</u> .		
2. 🔀 The allowed claim(s) is/are <u>1-21</u> .		
3. Acknowledgment is made of a claim for foreign priority under 35 U.S.C. § 119(a)-(d) or (f).  a) All b) Some* c) None of the:  1. Certified copies of the priority documents have been received.  2. Certified copies of the priority documents have been received in Application No.  3. Copies of the certified copies of the priority documents have been received in this national stage application from the International Bureau (PCT Rule 17.2(a)).  * Certified copies not received:  Applicant has THREE MONTHS FROM THE "MAILING DATE" of this communication to file a reply complying with the requirements noted below. Failure to timely comply will result in ABANDONMENT of this application.  THIS THREE MONTH PERIOD IS NOT EXTENDABLE.  4. A SUBSTITUTE OATH OR DECLARATION must be submitted. Note the attached EXAMINER'S AMENDMENT or NOTICE OF INFORMAL PATENT APPLICATION (PTO-152) which gives reason(s) why the oath or declaration is deficient.  5. CORRECTED DRAWINGS (as "replacement sheets") must be submitted.  (a) including changes required by the Notice of Draftsperson's Patent Drawing Review (PTO-948) attached  1) hereto or 2) to Paper No./Mail Date  (b) including changes required by the attached Examiner's Amendment / Comment or in the Office action of Paper No./Mail Date  Identifying indica such as the application number (see 37 CFR 1.84(c)) should be written on the drawings in the front (not the back) of each sheet. Replacement sheet(s) should be labeled as such in the header according to 37 CFR 1.121(d).  6. DEPOSIT OF and/or INFORMATION about the deposit of BIOLOGICAL MATERIAL must be submitted. Note the attached Examiner's comment regarding REQUIREMENT FOR THE DEPOSIT OF BIOLOGICAL MATERIAL.		
<ul> <li>Attachment(s)</li> <li>1. ☑ Notice of References Cited (PTO-892)</li> <li>2. ☐ Notice of Draftperson's Patent Drawing Review (PTO-948)</li> <li>3. ☑ Information Disclosure Statements (PTO-1449 or PTO/SB/Paper No./Mail Date 8/28/03 &amp; 1/15/04</li> <li>4. ☐ Examiner's Comment Regarding Requirement for Deposit of Biological Material</li> </ul>	6. ☐ Interview Summary Paper No./Mail Da 7. ☐ Examiner's Amend	ent of Reasons for Allowance  EILD  ENT FYAMINER

## **DETAILED ACTION**

## Allowable Subject Matter

Claims 1-21 are allowed. The following is an examiner's statement of reasons for allowance: Thomas et al. (U.S. Pat. No. 6,160,722) teaches a system for transferring power comprising a first power source (item 10) having a first pair of terminals associated therewith; a second power source (item 460) having a second pair of terminals associated therewith (i) a first pair of switches (items Q1 and Q2) connected to the first pair of terminals, while Thomas utilizes BJT transistors It would have been obvious to one of ordinary skill in the art at the time of the invention to use MOSFET having a source, drain and gate terminals connected as claimed. Thomas teaches terminal of the lower switch (Q2) thereby forming a first common node, and wherein each of the first pair of switches has an anti-parallel diode associated therewith; (ii) a second pair of switches (items Q2 and Q3) connected to the second pair of terminals, wherein said source terminal of said upper switch is electrically connected to the upper terminal of the lower switch thereby forming a second common node, and wherein each of the second pair of switches has an anti-parallel diode associated therewith; (iii) a third pair of switches (items Q3 and Q4), said third switch pair comprising an upper switch wherein terminal of said upper switch is electrically connected to the upper terminal of the lower switch thereby forming a third common node, and wherein each of the third pair of switches has an anti-parallel diode associated therewith; (iv) a first inductor (item

L1) a second inductor (item L2); and (vi) a control unit (item 440) configured to actuate the switches in the first, second, and third pairs of switches in accordance with one of a plurality of modes of operation for transferring power between at least a pair of said first, second. Thomas does not utilize three power sources nor does Thomas teach the limitations of the first and second inductors in the claimed connection. Nino (U.S. Pub. No. 20050218876) teaches a first inductor coupled between a first set of switches and a second set of switched but does not teach the second inductor or the additional power sources lacking in Thomas. At least for these further limitations to independent claims 1,8 and 15 are not taught or rendered obvious by the prior art.

Any comments considered necessary by applicant must be submitted no later than the payment of the issue fee and, to avoid processing delays, should preferably accompany the issue fee. Such submissions should be clearly labeled "Comments on Statement of Reasons for Allowance."

## Conclusion

Any inquiry concerning this communication or earlier communications from the examiner should be directed to Michael Rutland-Wallis whose telephone number is 571-272-5921. The examiner can normally be reached on Monday-Thursday 7:30AM-6:00PM EST.

If attempts to reach the examiner by telephone are unsuccessful, the examiner's supervisor, Lynn D. Feild can be reached on 571-272-2092. The fax phone number for the organization where this application or proceeding is assigned is 571-273-8300.

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Information regarding the status of an application may be obtained from the Patent Application Information Retrieval (PAIR) system. Status information for published applications may be obtained from either Private PAIR or Public PAIR. Status information for unpublished applications is available through Private PAIR only. For more information about the PAIR system, see http://pair-direct.uspto.gov. Should you have questions on access to the Private PAIR system, contact the Electronic Business Center (EBC) at 866-217-9197 (toll-free).

MRW

LYNN FEILD SUPERVISORY PATENT EXAMINER TECHNOLOGY CENTER 2800